



Search Results

BROWSE

SEARCH

IEEE XPLORE GUIDE

SUPPORT

Results for "((assertion)-in-ti)) <and> (pyr >= 1950 <and> pyr <= 2003)"

Your search matched 72 of 2272351 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by **Relevance** in **Descending** order.

**Join IEEE Now
Save 50%**

» Search Options

[View Session History](#)[New Search](#)

» Key

IEEE JNL	IEEE Journal or Magazine
IET JNL	IET Journal or Magazine
AIP JNL	AIP Journal
AVS JNL	AVS Journal
IEEE CNF	IEEE Conference Proceeding
IET CNF	IET Conference Proceeding
IEEE STD	IEEE Standard
	Indicates open access content

Modify Search

((assertion)-in-ti)) <and> (pyr >= 1950 <and> pyr <= 2003)

Search

☐ Check to search only within this results setDisplay Format: ☒ Citation ☐ Citation & Abstract

IEEE/ET/AIP/AVS

Books

Educational Courses

Application Notes

IEEE/ET journals, transactions, letters, magazines, conference proceedings, AIP/AVS journals, and standards.

view selected items

Select All Deselect All

View: 1-25 | 26-

☐ 1. A Scheme for Batch Verification of Integrity Assertions in a Data System

Lilien, Leszek; Bhargava, Bharat;
Software Engineering, IEEE Transactions on
Volume SE-10, Issue 6, Nov. 1984 Page(s):664 - 680
Digital Object Identifier 10.1109/TSE.1984.5010295

[AbstractPlus](#) | Full Text: PDF(4420 KB) IEEE JNL
[Rights and Permissions](#)

☐ 2. A Counter Example to an Assertion of Jackson

Levy, Sanford M.;
Electronic Computers, IEEE Transactions on
Volume EC-12, Issue 5, Oct. 1963 Page(s):573 - 573
Digital Object Identifier 10.1109/PGEC.1963.263663

[AbstractPlus](#) | Full Text: PDF(152 KB) IEEE JNL
[Rights and Permissions](#)

☐ 3. An Inductive Assertion Method for Register Transfer Level Design Verification

Pitchumani, V.; Stabler, E.P.;
Computers, IEEE Transactions on
Volume C-32, Issue 12, Dec. 1983 Page(s):1073 - 1080
Digital Object Identifier 10.1109/TC.1983.1676167

[AbstractPlus](#) | Full Text: PDF(3054 KB) IEEE JNL
[Rights and Permissions](#)

☐ 4. Analysis of real-time rule-based systems with behavioral constraints specified in Estella

Cheng, A.M.K.; Browne, J.C.; Mok, A.K.; Rwo-Hsi Wang;
Software Engineering, IEEE Transactions on
Volume 19, Issue 9, Sept. 1993 Page(s):863 - 885
Digital Object Identifier 10.1109/32.241770

[AbstractPlus](#) | Full Text: PDF(1716 KB) IEEE JNL

 **5. Order structure of symbolic assertion objects**

Brito, P.;
 Knowledge and Data Engineering, IEEE Transactions on
 Volume 6, Issue 5, Oct. 1994 Page(s):830 - 835
 Digital Object Identifier 10.1109/69.317710
[AbstractPlus](#) | [Full Text: PDF\(548 KB\)](#) | [IEEE JNL](#)
[Rights and Permissions](#)

 **6. Linear complexity assertions for sorting**

Saxena, N.R.; McCluskey, E.J.;
 Software Engineering, IEEE Transactions on
 Volume 20, Issue 6, June 1994 Page(s):424 - 431
 Digital Object Identifier 10.1109/32.295891
[AbstractPlus](#) | [Full Text: PDF\(660 KB\)](#) | [IEEE JNL](#)
[Rights and Permissions](#)

 **7. A practical approach to programming with assertions**

Rosenblum, D.S.;
 Software Engineering, IEEE Transactions on
 Volume 21, Issue 1, Jan. 1995 Page(s):19 - 31
 Digital Object Identifier 10.1109/32.341844
[AbstractPlus](#) | [Full Text: PDF\(1200 KB\)](#) | [IEEE JNL](#)
[Rights and Permissions](#)

 **8. Floating point fault tolerance with backward error assertions**

Boley, D.; Golub, G.H.; Makar, S.; Saxena, N.; McCluskey, E.J.;
 Computers, IEEE Transactions on
 Volume 44, Issue 2, Feb. 1995 Page(s):302 - 311
 Digital Object Identifier 10.1109/12.364541
[AbstractPlus](#) | [Full Text: PDF\(996 KB\)](#) | [IEEE JNL](#)
[Rights and Permissions](#)

 **9. Extending backward error assertions to tolerance of large errors floating point computations**

Fitzpatrick, P.;
 Computers, IEEE Transactions on
 Volume 46, Issue 4, April 1997 Page(s):505 - 510
 Digital Object Identifier 10.1109/12.588072
[AbstractPlus](#) | [Full Text: PDF\(64 KB\)](#) | [IEEE JNL](#)
[Rights and Permissions](#)

 **10. How assertions can increase test effectiveness**

Voas, J.;
 Software, IEEE
 Volume 14, Issue 2, Mar/Apr 1997 Page(s):118 - 119, 122
 Digital Object Identifier 10.1109/52.582981
[AbstractPlus](#) | [Full Text: PDF\(136 KB\)](#) | [IEEE JNL](#)
[Rights and Permissions](#)

 **11. Using word-level ATPG and modular arithmetic constraint-solving techniques for assertion property checking**

Chung-Yang Huan; Kwang-Ting Cheng;
 Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on
 Volume 20, Issue 3, March 2001 Page(s):381 - 391

Digital Object Identifier 10.1109/43.913756

[AbstractPlus](#) | [Full Text: PDF\(196 KB\)](#) | [IEEE JNL](#)
[Rights and Permissions](#)

12. Foundations of the trace assertion method of module interface specification

Janicki, R.; Sekerinski, E.;
 Software Engineering, IEEE Transactions on
 Volume 27, Issue 7, July 2001 Page(s):577 - 598
 Digital Object Identifier 10.1109/32.935852

[AbstractPlus](#) | [Full Text: PDF\(460 KB\)](#) | [IEEE JNL](#)
[Rights and Permissions](#)

13. Assertions: a personal perspective

Hoare, C.A.R.;
 Annals of the History of Computing, IEEE
 Volume 25, Issue 2, April-June 2003 Page(s):14 - 25
 Digital Object Identifier 10.1109/MAHC.2003.1203056

[AbstractPlus](#) | [Full Text: PDF\(534 KB\)](#) | [IEEE JNL](#)
[Rights and Permissions](#)

14. On Legality Assertions in Euclid

Wortman, D.B.;
 Software Engineering, IEEE Transactions on
 Volume SE-5, Issue 4, July 1979 Page(s):359 - 367

[AbstractPlus](#) | [Full Text: PDF\(2640 KB\)](#) | [IEEE JNL](#)
[Rights and Permissions](#)

15. Automated Protocol Validation in Argos: Assertion Proving and Searching

Holzmann, G.J.;
 Software Engineering, IEEE Transactions on
 Volume SE-13, Issue 6, June 1987 Page(s):683 - 696

[AbstractPlus](#) | [Full Text: PDF\(3568 KB\)](#) | [IEEE JNL](#)
[Rights and Permissions](#)

16. A Note on Synthesis of Inductive Assertions

Basu, S.K.;
 Software Engineering, IEEE Transactions on
 Volume SE-6, Issue 1, Jan. 1980 Page(s):32 - 39

[AbstractPlus](#) | [Full Text: PDF\(3344 KB\)](#) | [IEEE JNL](#)
[Rights and Permissions](#)

17. Program Correctness: On Inductive Assertion Methods








King, J.C.;
 Software Engineering, IEEE Transactions on
 Volume SE-6, Issue 5, Sept. 1980 Page(s):465 - 479

[AbstractPlus](#) | [Full Text: PDF\(4696 KB\)](#) | [IEEE JNL](#)
[Rights and Permissions](#)

18. Aladdin: Assembly Language Assertion Driven Debugging Interp

Fairley, R.E.;
 Software Engineering, IEEE Transactions on
 Volume SE-5, Issue 4, July 1979 Page(s):426 - 428

[AbstractPlus](#) | [Full Text: PDF\(1224 KB\)](#) | [IEEE JNL](#)

-  **19. On summarizing data using probabilistic assertions**
- Pearl, J.;
Information Theory, IEEE Transactions on
Volume 23, Issue 4, Jul 1977 Page(s):459 - 465
AbstractPlus | Full Text: PDF(1224 KB) IEEE JNL
[Rights and Permissions](#)
-  **20. Design and implementation of assertions for the Common Language Infrastructure**
- Tran, N.; Mingins, C.; Abramson, D.;
Software, IEEE Proceedings -
Volume 150, Issue 5, 27 Oct. 2003 Page(s):329 - 336
Digital Object Identifier 10.1049/ip-sen:20030988
AbstractPlus | Full Text: PDF(695 KB) IET JNL
-  **21. The Assertion of Possibilities in 'Indian-Jaina' Logic**
- Gupta, Madan M.;
American Control Conference, 1983
22-24 June 1983 Page(s):1240 - 1240
AbstractPlus | Full Text: PDF(57 KB) IEEE CNF
[Rights and Permissions](#)
-  **22. A three-tier assertion technique for SPICE verification of transistor timing analysis**
- Savithri, S.; Blaauw, D.G.;
VLSI Design, 1999. Proceedings. Twelfth International Conference On
7-10 Jan. 1999 Page(s):175 - 180
Digital Object Identifier 10.1109/ICVD.1999.745145
AbstractPlus | Full Text: PDF(56 KB) IEEE CNF
[Rights and Permissions](#)
-  **23. Efficient generation of monitor circuits for GSTE assertion graph:**
- Hu, A.-J.; Cases, J.; Jin Yang;
Computer Aided Design, 2003. ICCAD-2003. International Conference on
9-13 Nov. 2003 Page(s):154 - 159
Digital Object Identifier 10.1109/ICCAD.2003.1257620
AbstractPlus | Full Text: PDF(554 KB) IEEE CNF
[Rights and Permissions](#)
-  **24. Building software recovery assertions from a fault injection-based propagation analysis**
- Voas, J.;
Computer Software and Applications Conference, 1997. COMPSAC '97. Proceedings., The Twelfth Annual International
13-15 Aug. 1997 Page(s):505 - 510
Digital Object Identifier 10.1109/CMPSAC.1997.625059
AbstractPlus | Full Text: PDF(516 KB) IEEE CNF
[Rights and Permissions](#)
-  **25. Executable assertions and timed traces for on-line software error detection**
- Rabejac, C.; Blanquart, J.-P.; Queille, J.-P.;
Fault Tolerant Computing, 1996., Proceedings of Annual Symposium on
25-27 June 1996 Page(s):138 - 147

Digital Object Identifier 10.1109/FTCS.1996.534602

AbstractPlus | Full Text: PDF(864 KB) IEEE CNF
Rights and Permissions

View: 1-25 | 26-



[Help](#) [Contact Us](#) [Privacy & Security](#)

© Copyright 2008 IEEE -- All Rights Reserved